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STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number		10674642
Filing Date		2003-09-30
First Named Inventor	Levine et al.	
Art Unit	2192	
Examiner Name	Chou, Andrew Y.	
Attorney Docket Number	AUS920030484US1	

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AYL	1	5761103		1998-06-02	Oakland et al.	
AYL	2	6549959	B1	2003-04-13	Yates et al.	
AYL	3	6772322	B1	2004-08-03	Merchant et al.	
AYL	4	5937437		1999-08-10	Roth et al.	
AYL	5	6070009		2000-05-30	Dean et al.	
AYL	6	6381679	B1	2002-04-30	Matsubara et al.	
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AYL	8	6539458	B2	2003-03-25	Holmberg	

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AYC	9	6973542	B1	2005-12-06	Schmuck et al.	
AYC	10	6782454	B1	2004-08-24	Damron	
AYC	11	5652858		1997-06-29	Okada et al.	
AYC	12	6848029	B2	2005-01-25	Coldewey	
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AYC	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
AYC	2	"Cache Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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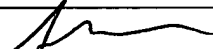
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